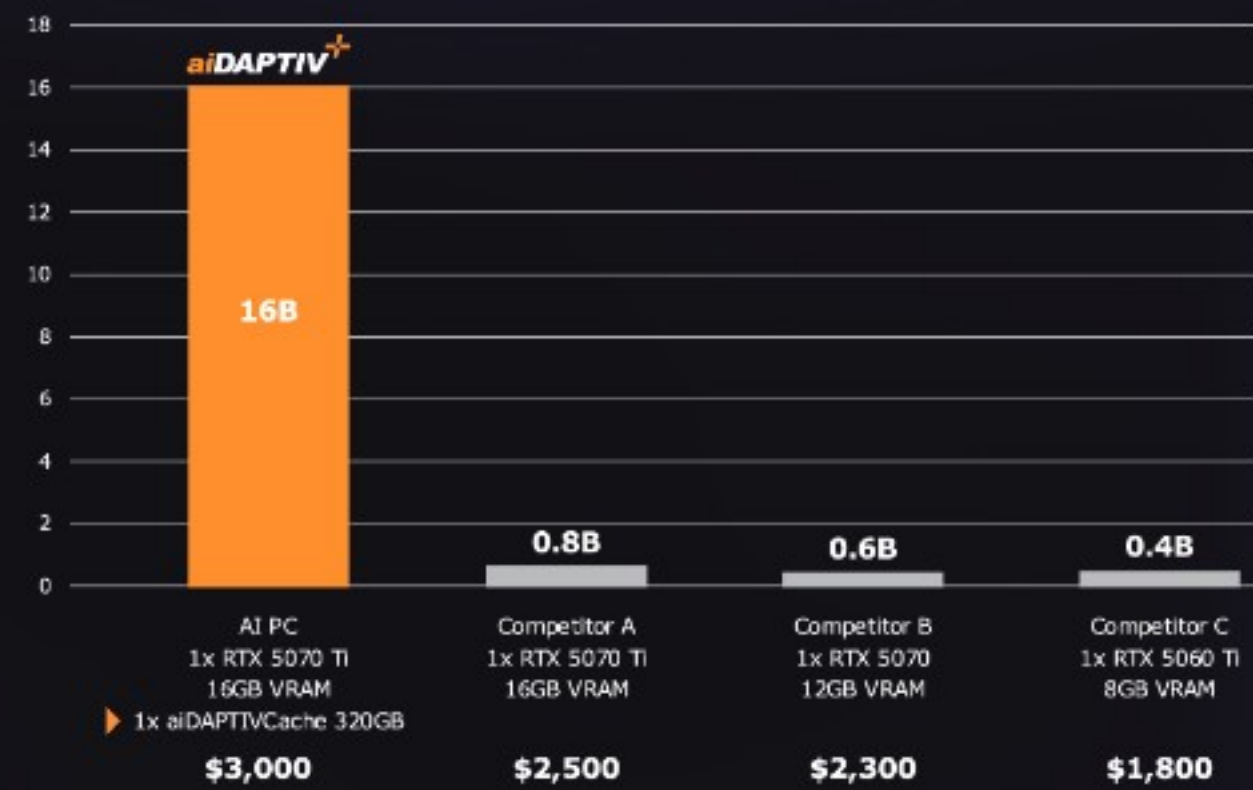


Unlock Large Model Training

Phison's aiDAPTIV+ solution enables significantly larger training models, giving you the opportunity to run AI processing that was previously too expensive to run on-premises or only reserved for the public cloud.

Capacity Boost for a 1-GPU Desktop PC with aiDAPTIV+



PHISON

LLM sizes: 16B CodeGen, 0.8B MobilLlama, 0.6B Qwen3, 0.4B EleutherAI Pythia

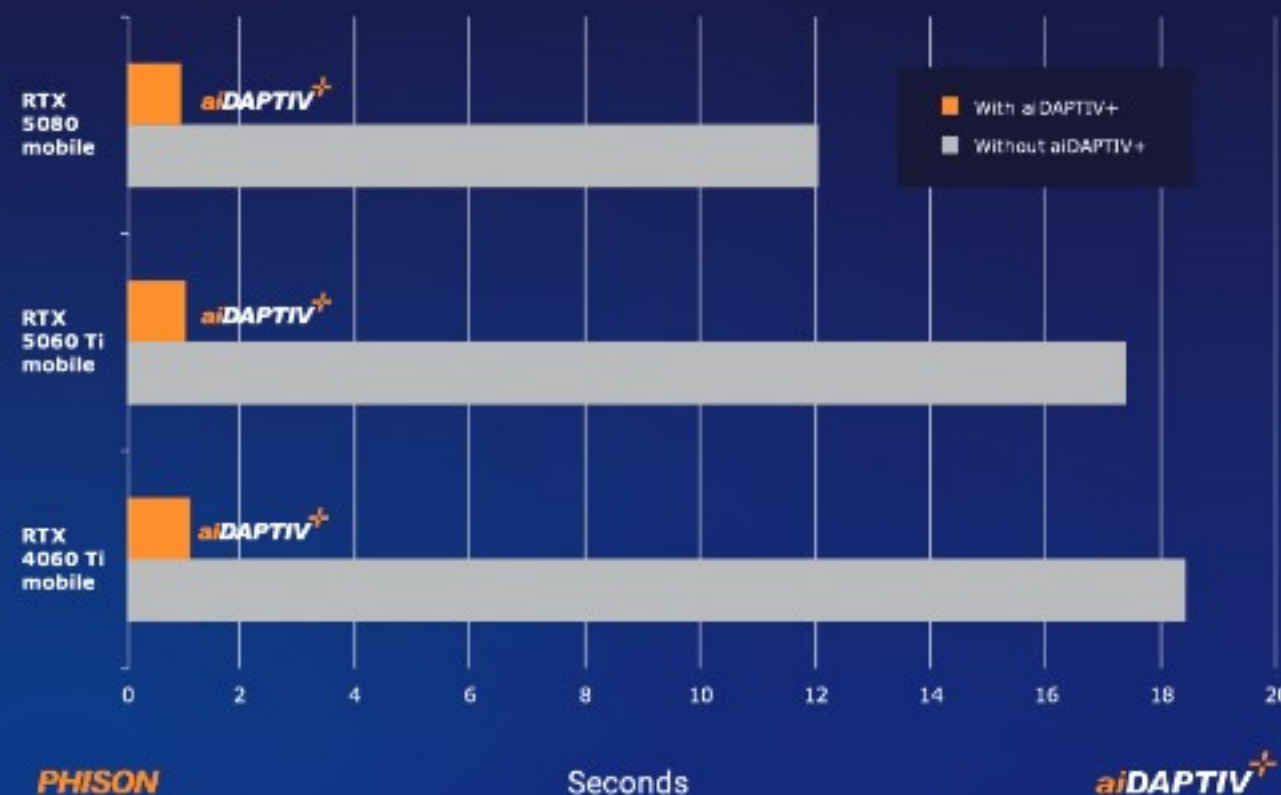
aiDAPTIV+

From Training to Chat

The software interface allows you to proceed from data ingest to fine-tune and RAG training to chat. In addition to enhanced LLM capacity, aiDAPTIV+ also improves inference performance for a better user experience.

10X Faster Inference performance with aiDAPTIV+

Time to First Token after KV Cache Pre-Fill (Smaller is Better)



PHISON

Seconds

aiDAPTIV+

Llama 3.1 8B Q4, the KV cache size of 4GB for 32K token length. TTFT with aiDAPTIV+ is minimally affected by computing power or number of GPUs.

Phison aiDAPTIV+ AI Processing Integrated Solution

Use a Command Line or leverage the intuitive All-in-One aiDAPTIVPro Suite to perform LLM Training

aiDAPTIVPro Suite



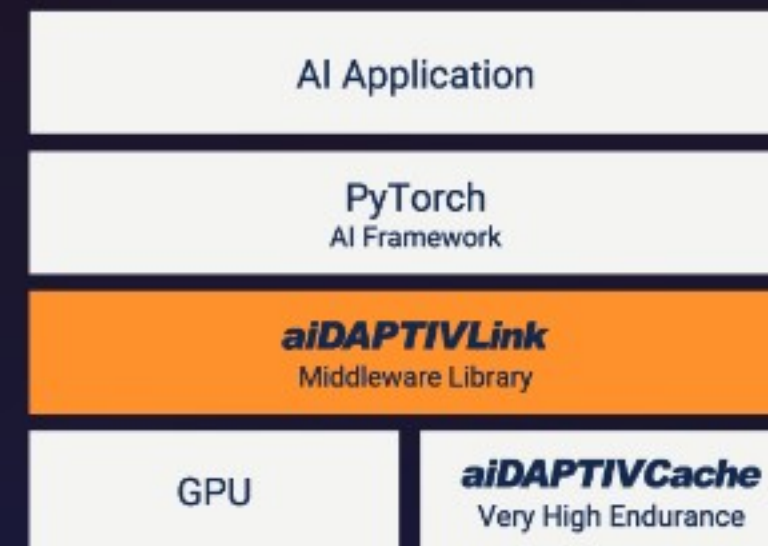
Supported Models

- Llama, Llama-2, Llama-3, CodeLlama
- Vicuna, Falcon, Whisper, Clip Large
- Metaformer, Resnet, Deit base, Mistral, TAIDE
- And many more being continually added



and/or

aiDAPTIVLink



Built-in Memory Management Solution

Experience seamless PyTorch compliance that eliminates the need to modify your AI application. You can effortlessly add nodes as needed. System vendors have access to aiDAPTIVCache SSDs, middleware library licenses, and full Phison support to facilitate smooth system integration.

and

Seamless Integration with GPU Memory

The optimized middleware extends GPU memory by an additional 80-320GB for IoT devices, 320GB-2TB for PCs, and 1-8TB for workstations and servers using aiDAPTIVCache. This added memory is used to support LLM training with low latency. Furthermore, the high endurance feature offers an industry-leading 100 DWPD, utilizing a specialized SSD design with an advanced NAND correction algorithm.

aiDAPTIVCache Family



A1100E M.2 AND U.2 SSDS